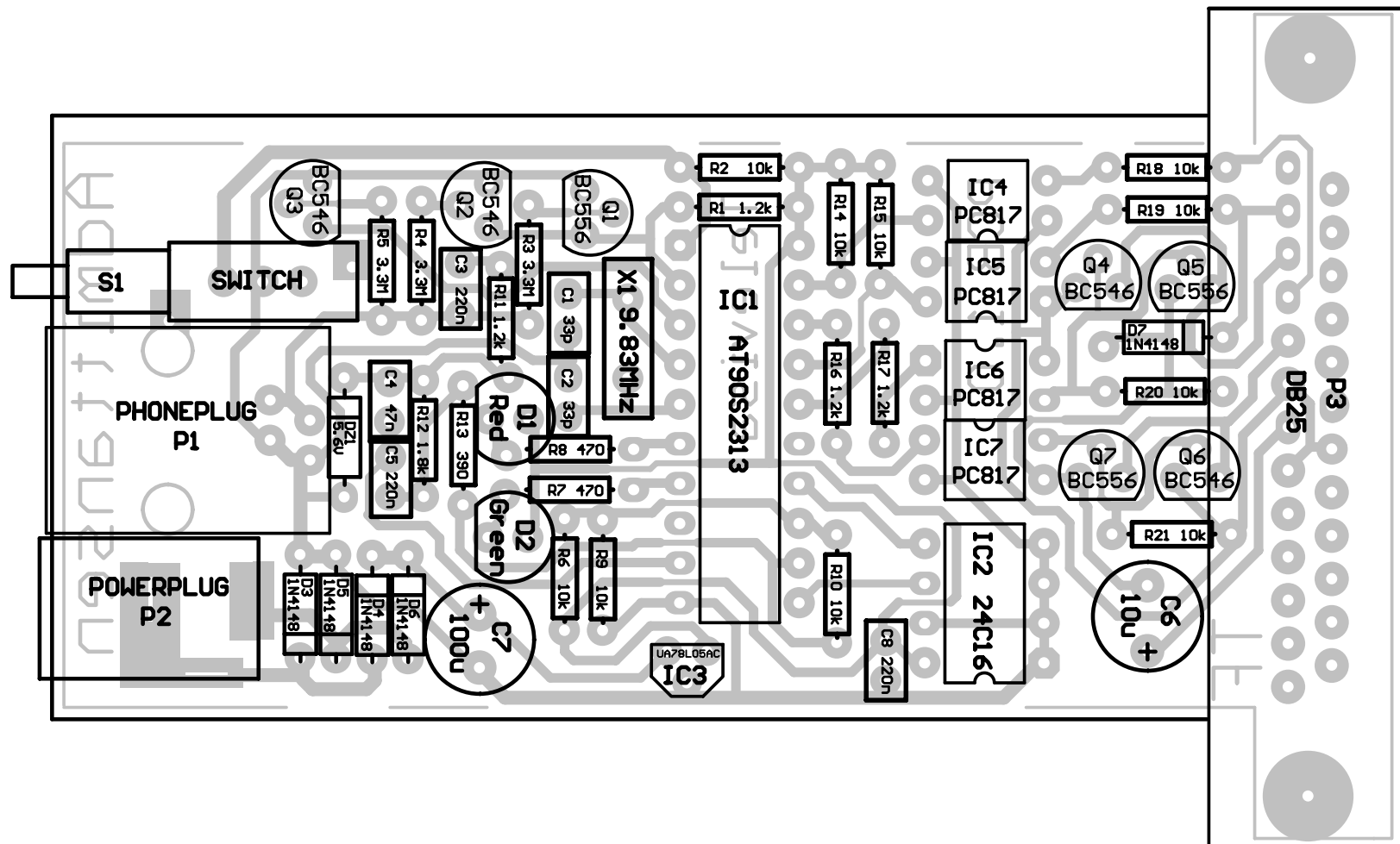
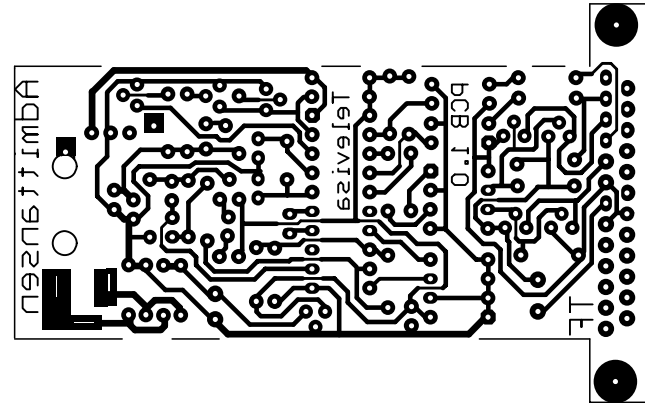
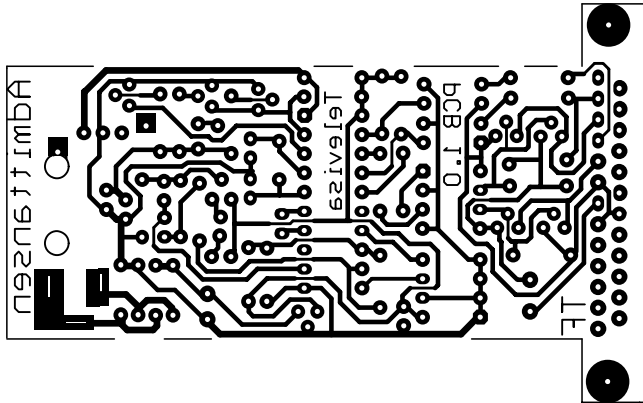
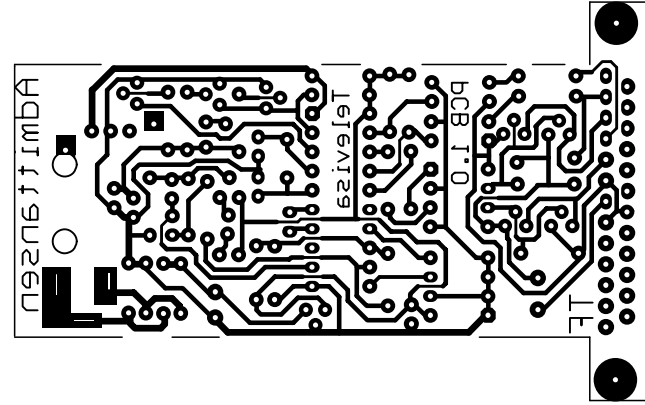
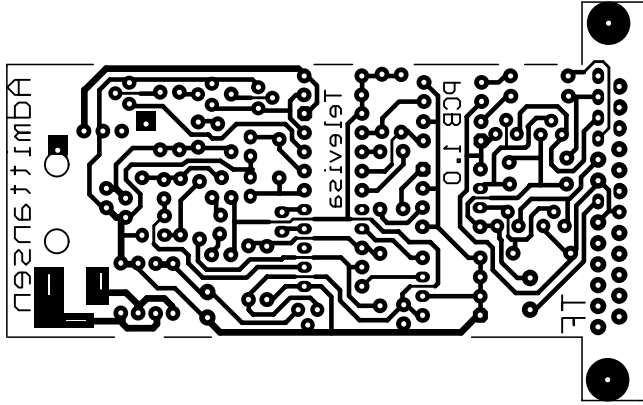


Title: Televisa 1.0	
Date: 2001-02-08	Revision: R1A
Designer: Tomas Franzon	

Televisa PCB 1.0 Top Layer



Televisa PCB 1.0 Bottom Layer



Admittansen
Televisa

S1 SWITCH

P1 PHONEPLUG

P2 POWERPLUG

C7 100u

IC2 24C16

IC1 AT90S2313

IC3 UM28L054C

X1 9.83MHz

R14 10k

R15 10k

R17 1.2k

R16 1.2k

IC4 PC817

IC5 PC817

IC6 PC817

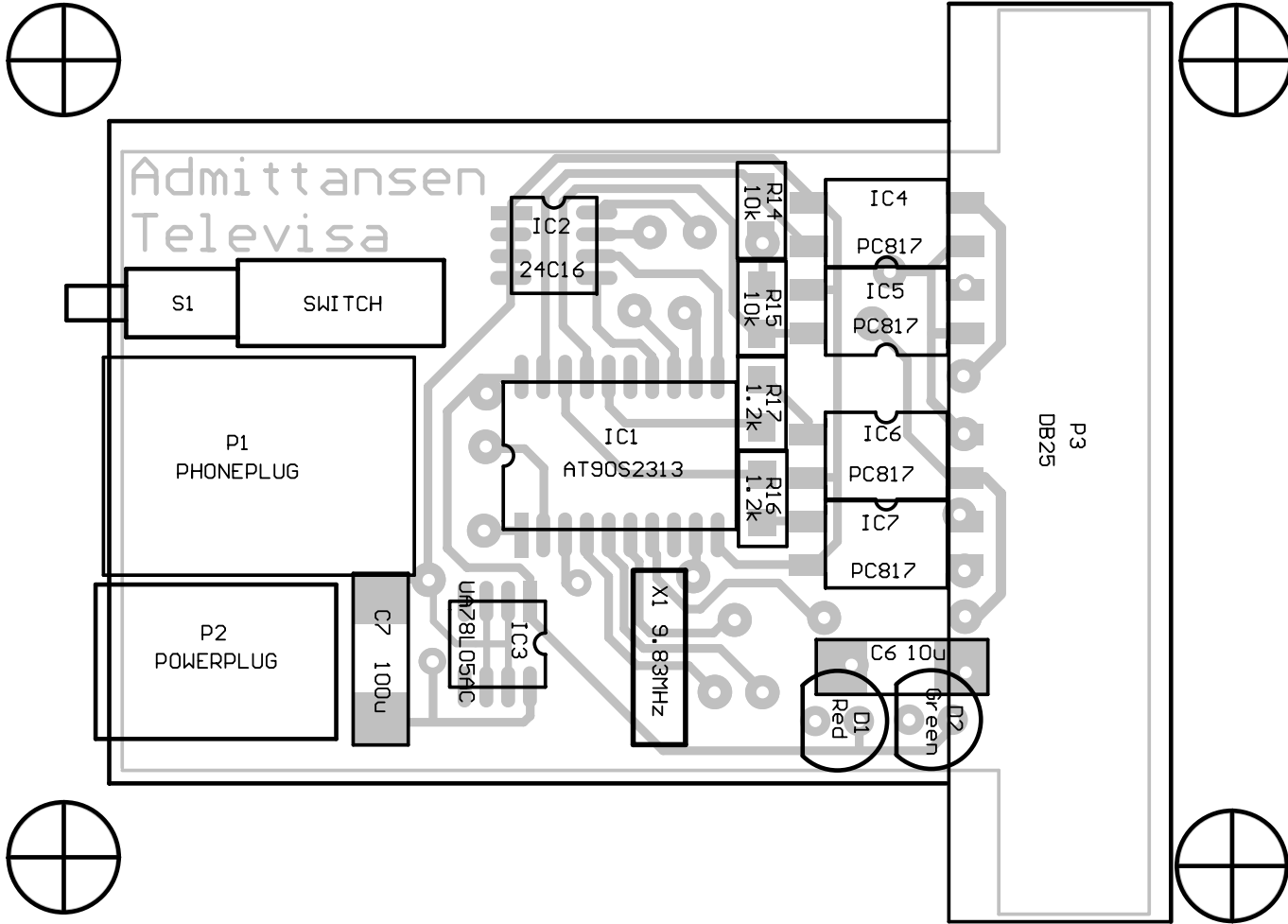
IC7 PC817

C6 10u

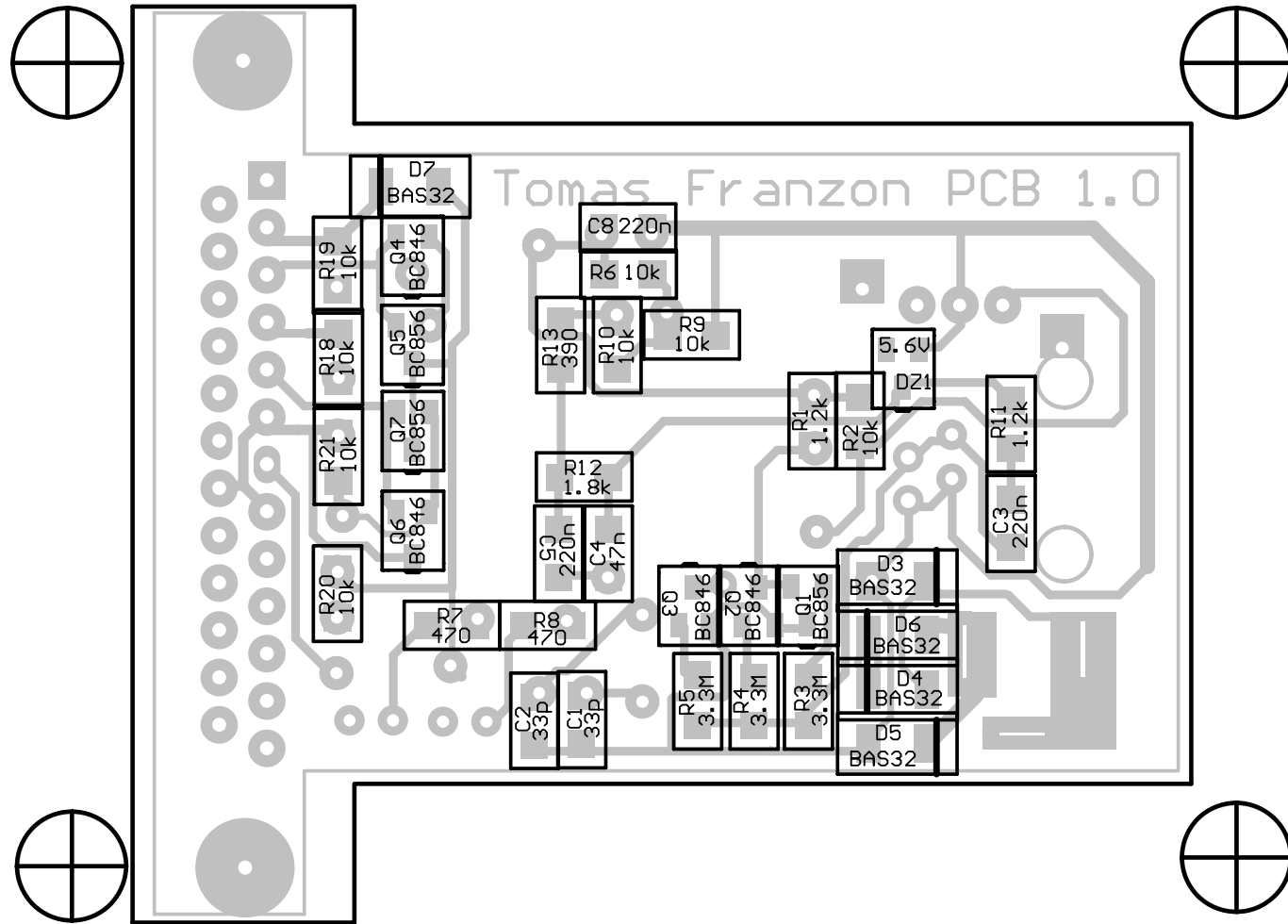
D1 Red

D2 Green

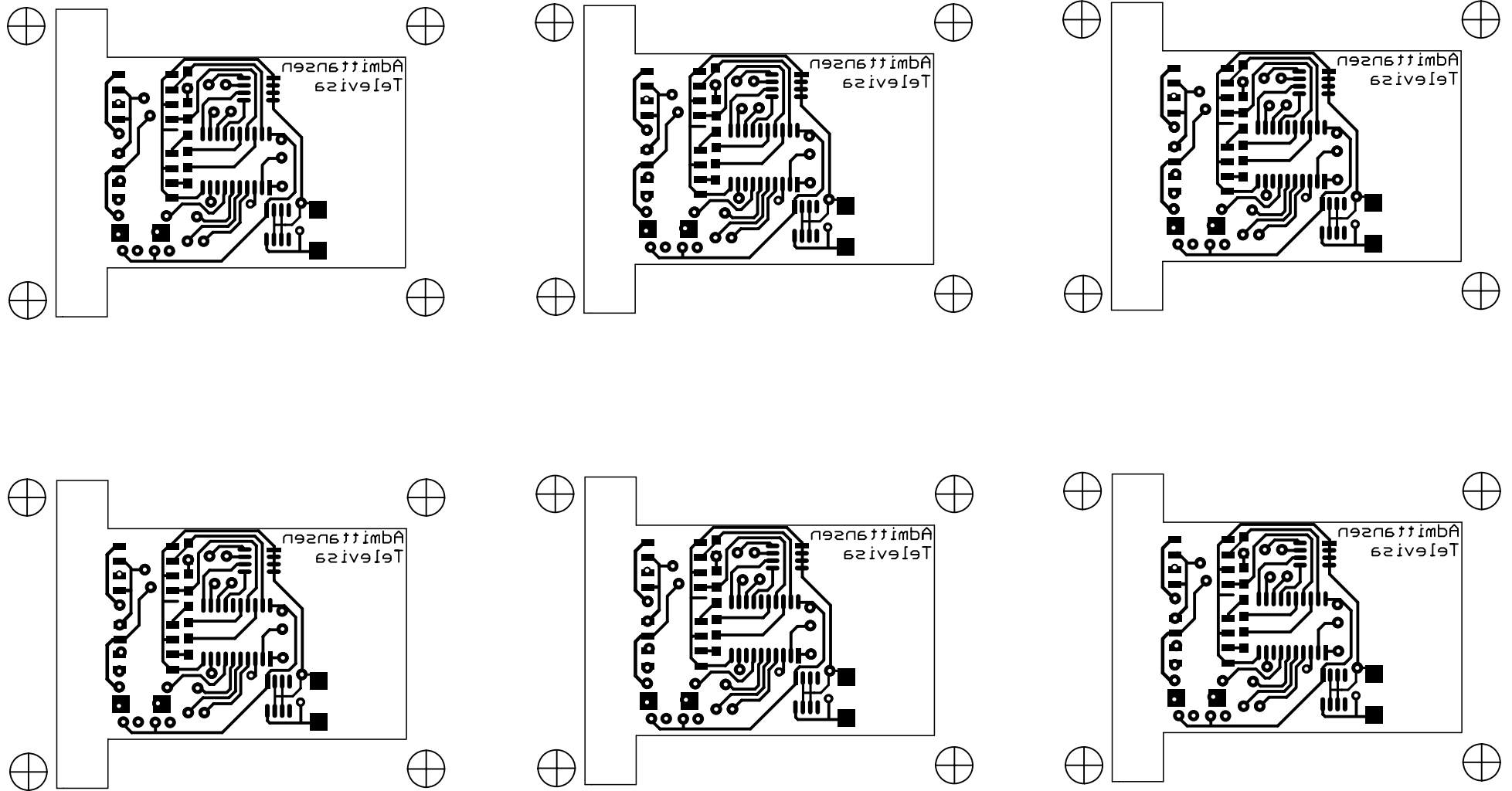
P3 DB25



Televisa SMD 1.0 Bottom View



Televisa SMD 1.0 Top Layer



Televisa SMD 1.0 Bottom Layer

