



AX6BC

User's Guide

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AX6BC Motherboard

User's Guide

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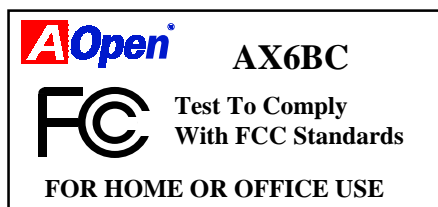
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FCC Class B Radio Frequency Declaration of Conformity

This equipment has been tested and found to comply with the limits for a Class B Subassembly -CPU Board device, pursuant to Part 15 of FCC Rules. Operation is subject to the following two conditions: 1. This device may not cause harmful interference, and 2. This device must accept any interference received, including interference that may cause undesired operation.

Equipment Classification : FCC Class B Subassembly - CPU Board
Type of Product : Intel Pentium II Micro ATX Motherboard
Chipset(s) : Intel 82440BX



The limits of FCC Part 15 Class B are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

1. Reorient or relocate the receiving antenna.
2. Increase the separation between the equipment and receiver.
3. Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
4. Consult the dealer or an experienced radio/television technician for help.

Notice 1:

The changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Notice 2:

Shielded interface cables, if any, must be used in order to comply with emission limits.

Organization

Chapter 1, **Overview**, covers the introduction and specifications of the system board and special features.

Chapter 2, **Hardware Installation**, describes hardware jumpers, connectors and memory configuration. There are user friendly drawings to locate jumper and connector.

Chapter 3, **AWARD BIOS**, explains the system BIOS and tells how to configure the system by setting the BIOS parameters.

Appendix A, **Frequently Asked Question**, collects most frequently asked question of this product.

Appendix B, **Troubleshooting Guide**, includes first aid information you need if you meet trouble, the WWW address and worldwide service telephone/fax are also included.

Appendix C, **Jumper Table Summary**, gives you a tabular summary of the jumper settings discussed in Chapter 2.

Conventions

The following conventions are used in this manual:

Text entered by user, default settings, recommended selections	Represent text input by the user, default settings and recommended selections
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<Enter>, <Tab>,<Ctl>, <Alt>, <Ins>, , etc	Represent the actual keys that you have to press on the keyboard.
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Note:
Gives bits and pieces of additional information related to the current topic.



Warning:
Alerts you to any damage that might result from doing or not doing specific actions.



Caution:
Suggests precautionary measures to avoid potential hardware or software problems.



Important:
Reminds you to take specific action relevant to the accomplishment of the procedure at hand.



Tip:
Tells how to accomplish a procedure with minimum steps through little shortcuts.

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APPENDIX B TROUBLESHOOTING

APPENDIX C JUMPER TABLE SUMMARY